CLAIMS

Having thus described my invention, what I claim as new and desire to secure by Letters Patent is as follows:

1. A method of pre-processing image data, said method including steps of

applying luminance and chrominance data of consecutively presented lines of data to respective data inputs of a filter, and

applying hybrid filter coefficients to said filter to concurrently obtain spatially filtered and chrominance converted data.

2. A method as recited in claim 1, wherein said consecutively presented lines are lines of a progressive scan format.

3. A method as recited in claim 1, wherein said consecutively presented lines are lines of an odd field or an even field of an interlaced scan format.

4. A method as recited in claim 3, further including a step of

altering said hybrid filter coefficients for respective ones of said odd field and said even field.

5. A method as recited in claim 1, further including a step of

removing alternate lines of said chrominance converted data.

6. A method as recited in claim 1, including the further steps of

multiplying said luminance and chrominance data by said hybrid filter coefficients for respective ones of said consecutively presented lines to produce weighted luminance and chrominance values, and

summing said weighted luminance and chrominance values.

7. A pre-processing circuit for image data including

a filter having inputs to receive luminance and chrominance data corresponding to consecutive image data lines, and

means for applying hybrid filter coefficients to said filter such that spatially filtered and chrominance converted data are concurrently developed by said filter.

8. A pre-processing circuit as recited in claim 7, further comprising

a buffer for storing said consecutive lines of said image data and outputting said image data to said filter.

9. A pre-processing circuit as recited in claim 7, wherein said consecutive image data lines correspond to a progressive scan format.

10. A pre-processing circuit as recited in claim 7, wherein said consecutive image data lines correspond to and odd field or an even field of an interlaced scan format.

11. A pre-processing circuit as recited in claim 10, further including

means for altering said hybrid filter coefficients for respective ones of said odd field and said even field.

12. A preprocessing circuit as recited in claim 7, further including

means for sub-sampling said chrominance converted data.